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Monday 13/03/2006

14H00-15H00 : G.CLAUS

- IPHC (IReS) MAPS test group presentation
- Planning for the week
- DAQ hardware and software available for EUDET
- IPHC ADC boards performances and limitations

15H00-15H30 : Pause

15H30-17H30 : G.CLAUS

- Mimo* 2 control
        - Digital sequence
        - Analogue output
        - JTAG

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Tuesday 14/03/2006

9H00-10H30 : G.CLAUS

- How to use DAQ software

10H30-11H00 : Pause

11H00-12H30 : G.CLAUS, M.GOFFE

- DAQ software and Mimo* 2 test bench demonstration

14H00-17H30 : M.SZELEZNIAK

- MAPS calibration : data analysis algorithms
- LabView analysis program demonstration

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Wednesday 15/03/2006

Two sessions running in parallel.

Session 1 :

Team involved in Mimosa chips calibration (DESY, …) should choose this session.

9H00-17H30 : M.GOFFE

MAPS calibration and data analysis.
Demonstration of data taking and processing for Mimo* 2 and Mimosa 5 calibration.

Session 2 :

Team involved in DAQ development (Geneve and Ferrara, …) should choose this session.

9H00-17H30 : G.CLAUS, K.JAASKELAINEN

IPHC ADC board DAQ and JTAG programming.
ADC board and JTAG libraries explanation and demonstration application examples.

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Thursday 16/03/2006

9H00-12H30 : G.CLAUS, M.GOFFE, K.JAASKELAINEN

IPHC USB DAQ demonstration : simulation of Mimostar 3M acquisition.
Trigger handling and multiple ADC boards control, if it's ready ! (see remarks section)
It can also be an extension of Wednesday sessions 1 and 2 if neeeded.

14H00-15H30 : C.Hu

- Mimo* 2 Polarisation tuning
- Mimo* 3M Architecture ?

15H30-16H00 : Pause

16H00-17H30 : Free for questions
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Friday 17/03/2006

9H00-12H30 : M.WINTER, W.DULINSKI, …

- **Summary**

Hardware and software available at the end of week 11 :

GENERAL HW & SW

- **2 USB ADC boards : one for Geneve, the other for DESY**

- **3 Mimostar 2 calibrated and mounted on a pcb board**

- **Calibration results of each Mimostar 2 chip**

- **The CMOS group USB DAQ software (Windows executable file)**
    - **It controls ADC board parameters and handles configurations**
    - **It plots mimosa RAW data (Frame 0, 1, CDS) on a rudimentary display**
    - **It stores data in binary files**

- **The CMOS group USB DAQ software data file format documentation**

- **The CMOS group Mimostar 2 slow control (JTAG) software (Windows executable file)**

- **The CMOS group Mimosa Analysis and calibration software (Windows Labview executable file)**

DAQ SW

- **ADC USB board control DLL (Windows) with C source code**

- **ADC USB board demonstration application (using the DLL) with source code**

- **JTAG slow control DLL (Windows) with C source code**

- **JTAG slow control demonstration application (using the DLL) with source code**

Remarks :

- **The DLL and demonstration applications had been developed with C++ Builder 6, it seems possible to download free version limited to two month from Borland web site. If this information is confirmed, therefore you will be able to build demonstrations examples and "play"with them without any extra cost. Later you can go ahead in Borland's direction or choose another IDE.**

- **Eleuterio Spiriti can bring back his USB ADC board to Strasbourg, therefore we can upgrade the board with last firmware version. This will allow us to have one more USB ADC board in the EUDET collaboration.**

- **About trigger handling by USB ADC board … it has been implemented in VHDL firmware but it is not tested yet. Therefore i don't know if we will be ready to present it on week 11.**