

## Work Breakdown Structure for DAQ for JRA1, Version0, 14 December 2005

- 1st version of WBS for Demonstrator:
  - Milestones:
    - 1 Design, April 1, 2006:
    - 2 Construction, January 2007
    - 3 Delivery, July 2007
  - Deliverables 1:
    - Strasbourg:
      - Existing raw data (noise, source, telescope)
      - MIMOSTAR-2 x 3
      - USB Board w/o D/R (1 + 1 later)
      - User training: March 13 - 17 (date to be reiterated)
    - Geneva:
      - Design of data & program structure
      - algorithms
      - interprocess communication
      - TLU design
    - Milano:
      - Board schematic
      - FPGA LVDS design
      - Interface definition
    - Bonn/Mannheim:
      - DAQ Code, "Physicist Documentation"
  - Deliverables 2:
    - Strasbourg:
      - Delivery of  $\geq 2$  MIMOSTAR-3M, thinned to 120 microns, Integrated on front-end, fully alive
      - Spare USB board (D/R not mandatory)
    - Milano:
      - EURDB prototype board functional (D/R not mandatory)
      - mother + daughter for MIMOSTAR-2
    - Geneva:
      - Close to full DAQ for demonstrator
      - TLU
    - Bonn/Mannheim
      - Dataproducer and monitoring for DUT